PATENT ABSTRACTS OF JAPAN

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(54) SKEW CORRECTING CIRCUIT

(57)Abstract:
PURPOSE: To shorten the control time for skew by detecting the phase difference of output signals between the 1st and 2nd delay circuits and controlling the delay time of the 2nd delay circuit in accordance with the detected phase difference.
CONSTITUTION: An input signal I0 is delayed by a delay circuit 10 and outputted as an output signal Q0, and an input signal I1 is delayed by a delay circuit 11 of a control circuit 101 and outputted as an output signal Q1. A phase comparator 21 compares the phases of both signals Q0 and Q1 with each other and sets a phase difference detecting signal DW at an active level when the signal Q1 is delayed to the signal Q0. An up-down counter 31 reduces the count value by '1'. Thus the delay time of the circuit 11 is shortened, and the phase of the signal Q1 approximates to that of the signal Q0. As a result, the skew is automatically controlled and the control time for skew is also shortened.